FIG. 1

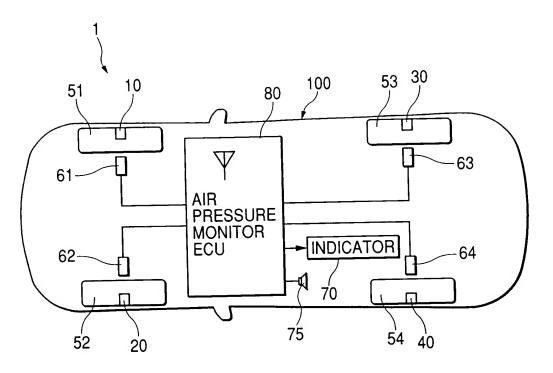


FIG. 2A

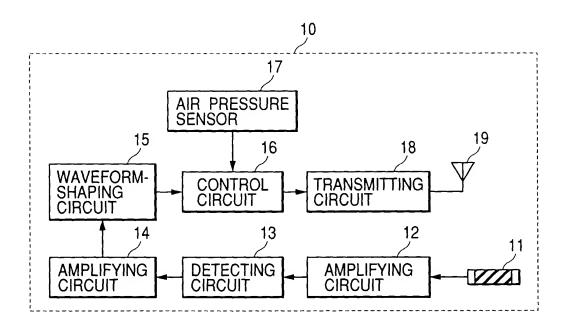


FIG. 2B 100 80 70 71 7,1 81 82 83 FL 1 FR | [] RECEIVING CIRCUIT CONTROL CIRCUIT 61 DRIVE CIRCUIT I/F CIRCUIT 1 RL RR, 71 71 85 84

FIG. 3

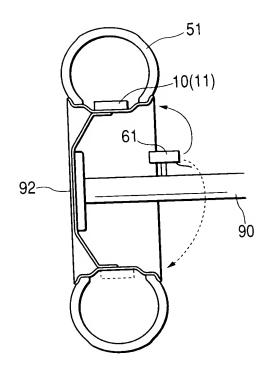


FIG. 4

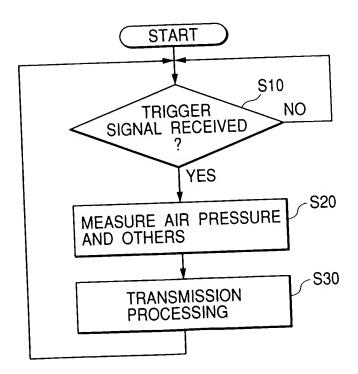


FIG. 5

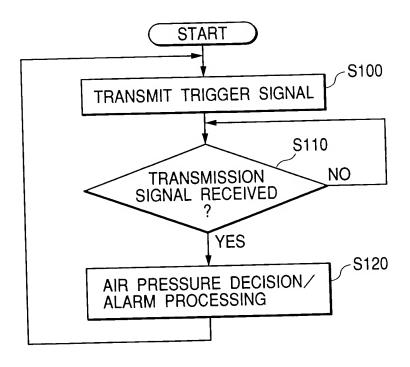


FIG. 6

